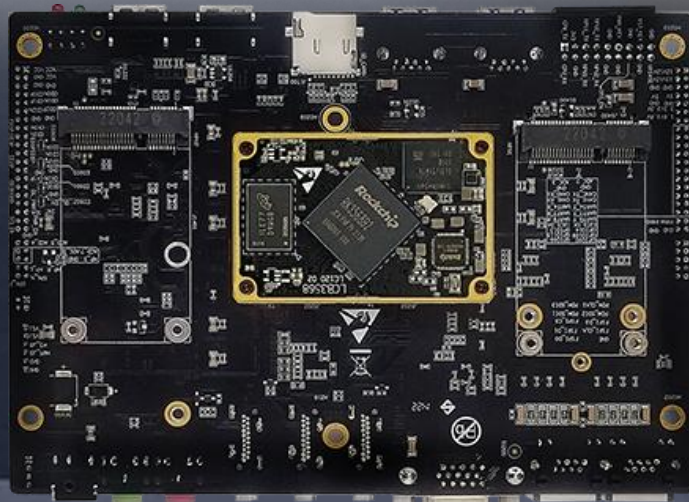


neardi

# LKD3568 Development Board

## Datasheet

### V1.0



Shanghai Neardi Technology Co., Ltd.

[www.neardi.com](http://www.neardi.com)

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Due to product version upgrades or other needs, our company may update the manual. If you need the latest version of the manual, please contact our company. We always adhere to the principle of customer first and provide customers with fast and efficient support services. If you have any needs, please feel free to contact our company at any time. Contact information is as follows:

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## Version History

Version	Date	Description
V1.0	2022/8/23	Initial version

# Contents

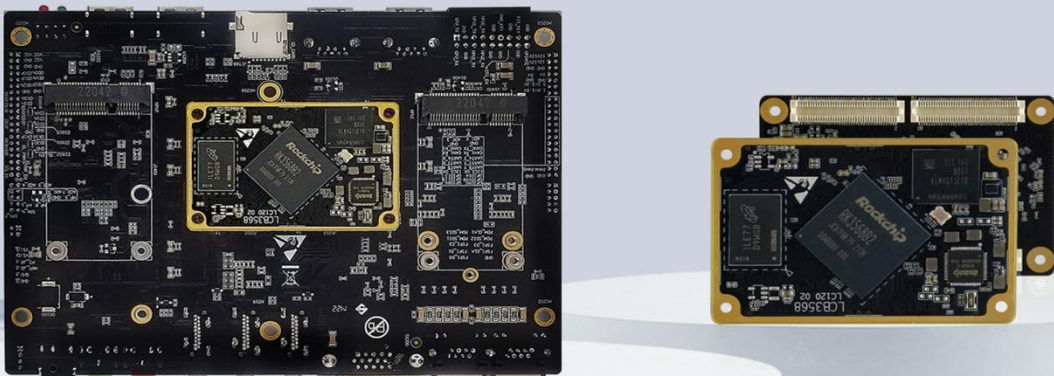
- 1.Product Introduction .....3**
- 2.Function Overview ..... 4**
- 3.Technical Specifications ..... 7**
- 4.Appearance and Dimensions ..... 10**
- 5.Interface Definition ..... 11**
- 6.Pin Definition ..... 13**
- 7.Application Scenarios ..... 20**
- 8.Ordering Model ..... 21**
- 9.About Neardi ..... 22**

# 1.Product Introduction

The LKD3568 is a multifunctional industry application board exquisitely designed based on the Rockchip RK3568 chip platform, consisting of our company's LCB3568 core module and baseboard. The core module is connected to the baseboard using a B2B connector and secured with four M2 screws, ensuring stability and reliability. The board is versatile and rich in interfaces, suitable for a wide range of applications.

The LKD3568 features 1 USB 3.0 HOST, 1 USB 3.0 OTG, and 2 USB 2.0 HOST interfaces, capable of connecting multiple USB cameras externally. It also has 3 mini-PCIe interfaces that can be expanded to connect 4G/5G modules or NPU computing cards with mini-PCIe interfaces based on RK1808, combining with multiple cameras to form an artificial intelligence vision computing board card that supports up to 6.0 TOPS of computational power. Additionally, the LKD3568 supports dual-band WIFI 6, BT5.0, 2 Gigabit Ethernet, UART, I2C, CANBUS, and other common communication module interfaces. It supports 2 HDMI outputs, 1 VGA output, 1 dual-channel LVDS, and various display interfaces, supporting multi-screen independent display. Optional configurations can include 1 HDMI input or MIPI-CSI camera input.

The LKD3568 supports Android, buildroot, Debian, and Ubuntu operating systems, offering advantages such as high performance, high reliability, and high scalability, and provides users with open system source code. Users can develop and customize based on this product, and our company provides comprehensive technical support for developers and enterprise users, enabling them to efficiently complete research and development work and significantly shorten the product development and mass production cycle.



## 2. Function Overview



### High-Performance Processor

<b>CPU</b>	RK3568, 22nm process, quad-core 64-bit Cortex-A55, with a maximum clock speed of up to 2.0GHz.
<b>GPU</b>	ARM G52 2EE, with integrated high-performance 2D acceleration hardware
<b>NPU</b>	1TOPS
<b>VPU</b>	4K video decoding, 1080P video encoding
<b>DDR</b>	LPDDR4 memory, with options for 1GB, 1GB, 4GB or 8GB capacities.
<b>eMMC</b>	eMMC 5.1 storage, with options for 8GB,16GB,32GB,64GB,or128GB capacities.



### Rich Interfaces

Multiple display interfaces: VGA, HDMI 1.4, HDMI 2.0, dual-channel LVDS, supporting multi-screen independent display.

3 UART interfaces and 1 I2C interface, with support for expanding 2 CANBUS interfaces.

2 Gigabit Ethernet ports, dual-band WIFI 6.

3 MIPI PCIe interfaces, expandable for 4G/5G modules, expandable for computational power cards.

1 M.2 M-Key interface, supporting external NVMe protocol SSDs.

1 Type-A USB 3.0 HOST, 1 Type-C USB 3.1 OTG, 2 USB 2.0 interfaces.



## 可扩展 NPU 算力

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NPU computational power can be expanded up to 10 TOPS; capable of externally connecting three 3 TOPS computational cards.

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Demo programs are provided.

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## Operating System

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Android

Linux (Buildroot / Debian / Ubuntu)

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## Open Source Materials

WIKI Documentation     <http://www.neardi.com/cms/en/wiki.html>

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Quick Start

Firmware Upgrade

Android Development

Linux Development

Kernel Drivers

DEMO

System Customization

Accessories

Frequently Asked Questions (FAQ)

Release Notes

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## Hardware Materials

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Chip Datasheet

Product 2D/3D Drawings

Core Board Pin Definitions

Baseboard Reference Schematic

Baseboard Reference PCB

Key Bill of Materials (BOM)

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## Software Materials

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Firmware Tools and Drivers

Android Source Code and Images

U-Boot and Kernel Source Code

Debian/Ubuntu/Buildroot System Files

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## 3. Technical Specifications

### Basic Parameters

SOC	RK3568, 22nm process, quad-core 64-bit Cortex-A55, with a maximum clock speed of 2.0GHz.
GPU	ARM G52 2EE, supports OpenGL ES 1.1/2.0/3.2, OpenCL 2.0, Vulkan 1.1, and has a high-quality 2D graphics engine built-in..
NPU	Offers up to 1 TOPS of computational power; supports hybrid operations of INT8/INT16/FP16/BFP16 MAC; compatible with deep-learning frameworks such as TensorFlow, TF-lite, Pytorch, Caffe, ONNX, MXNet, Keras, and Darknet.
VPU	Capable of 4K VP9 and 4K H265 video decoding at up to 60fps.  Capable of 1080P H265/H264 video encoding at up to 100fps.  Equipped with an 8M ISP with HDR capabilities.
DDR	LPDDR4 RAM, with options for 1GB/2GB/4GB/8GB (Optional).
eMMC	eMMC 5.1 storage, with options for 8GB/16GB/32GB/64GB/128GB (Optional).
PMU	RK806
OS	Android / Ubuntu / Buildroot / Debian

### Hardware Specifications

Power	DC12V - 3A (DC Jack 5.5*2.1mm / PH2.0 wafer connector)
USB	1*Type-A USB3.0 HOST,  1*Type-A USB3.0 OTG,



	2*Type-A USB2.0 HOST
Display output	Type-A HDMI 2.0 up to 4K@60fps
	Type-A HDMI 1.4 up to 1920*1080@60fps
	VGA up to 1920*1080@30fps
	Duel channel LVDS up to 1080P@60HZ
Audio	φ3.5mm earphone Jack with L/R audio out
	φ3.5mm microphone Jack with Mic in
	Speaker output with 1.5W@8Ω
	HDMI audio out
Display input	HDMI Input interface; MIPI-CSI Camera Interface
Mini-PCIe	1*mini PCIe for 2G/3G/4G/5G LTE module,
	2*mini PCIe for AI cards
M.2	M.2 NGFF ( M-KEY ) PCIE V2.1 x4 with NVMe SSD supported
SD card	Compatible with SDIO 3.0 protocol, system boot up supported
SIM card	Micro sim slot for Mini-PCIe 4G LTE module
RJ-45	2*10/100/1000 Ethernet
RTC	2Pin GH1.25 connector, RTC power on and off supported
Others	3*Uart, 2*CAN BUS, 1*I2C, 6*ADC, 2*PWM, lots of GPIOs
Power output	12V, 5V, 3.3V
Others	4*ADC, 1*I2C(CTP supported), 4*Switch signal Input, 4*Switch signal Output,

### Other Parameters

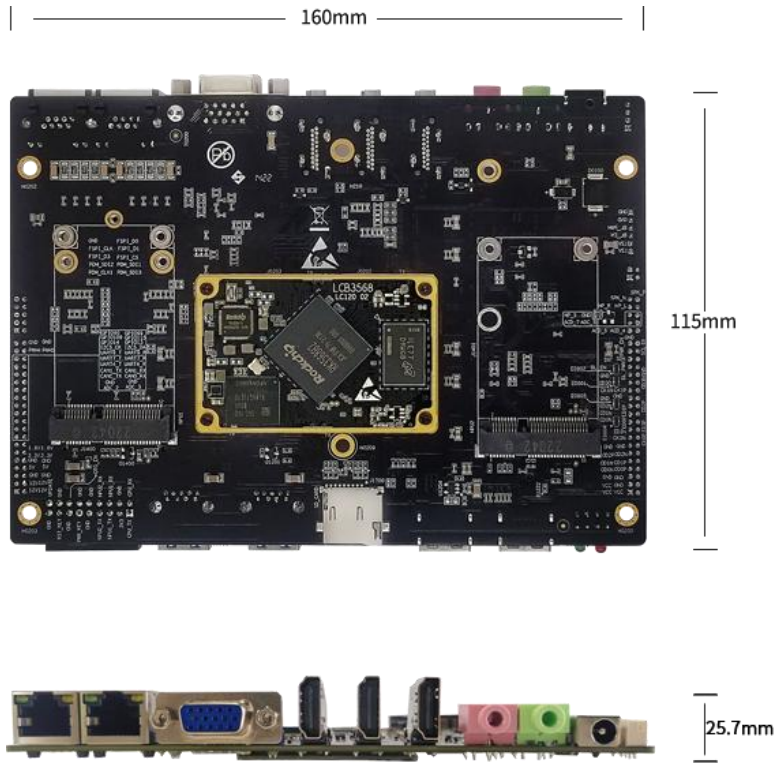
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Dimensions	Length * Width * Height (mm) 160*115*25.7
Operating Temperature	-10 ~ 70°C
Weight	Approximately 171.5g (excluding peripherals)

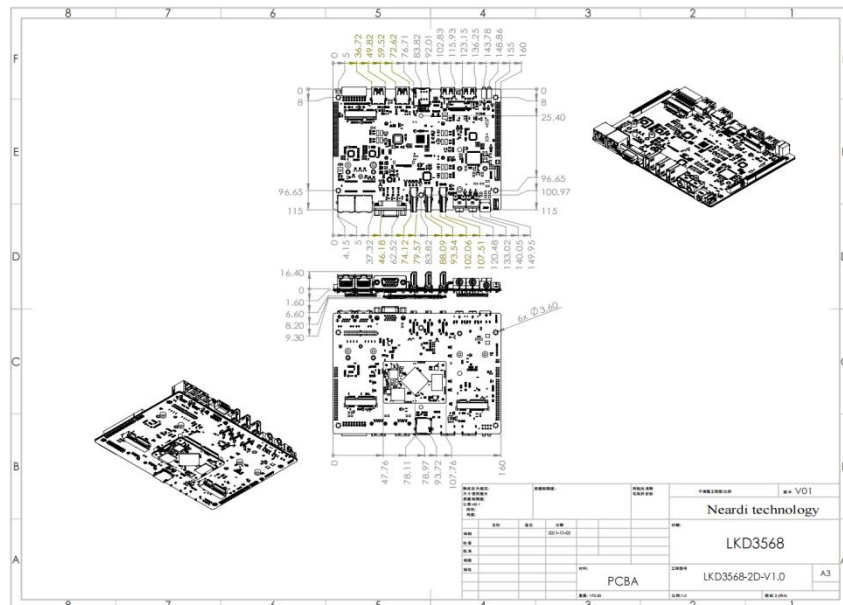
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# 4. Appearance and Dimensions

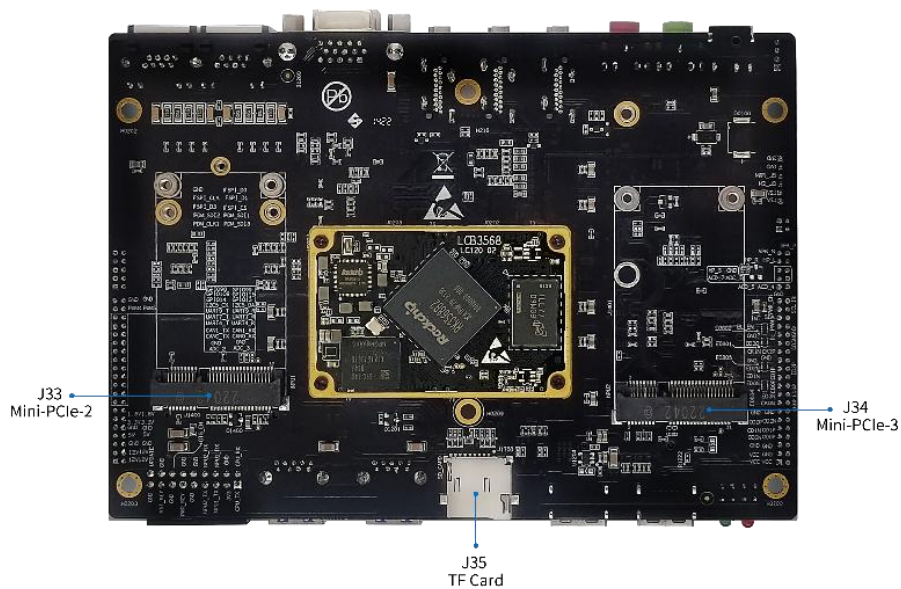
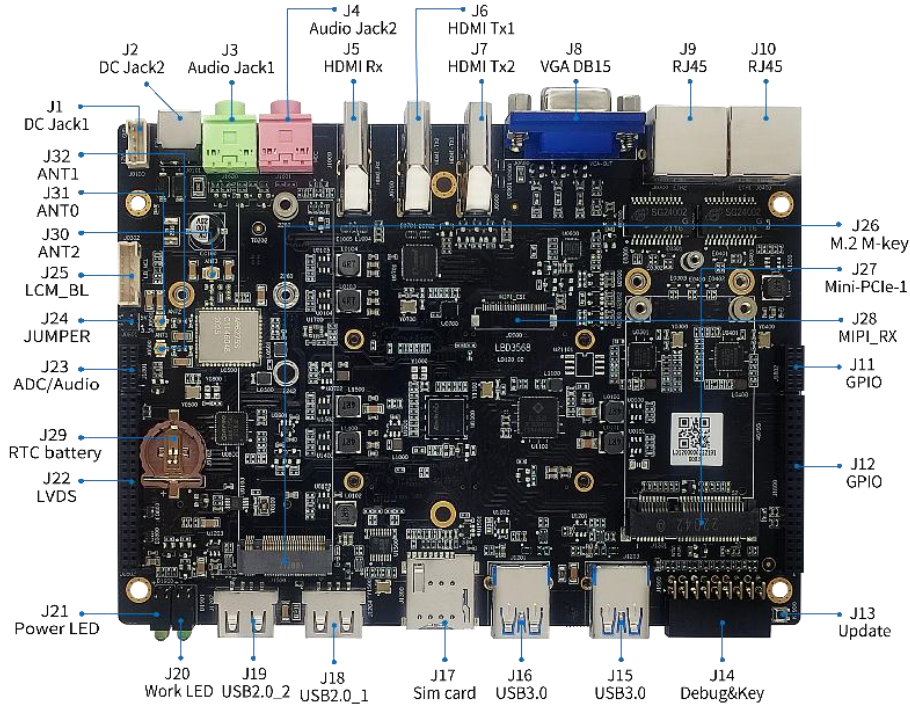
## 4.1 Appearance



## 4.2 Dimensions



# 5. Interface Definition



Part reference	Part Name	Part Specifications	Part Description
J1	DC Jack1	PH2.0mm 4pin wafer	DC12V-3A power in
J2	DC Jack2	DC 5.5*2.1mm	Main power supply, DC12V – 3A
J3	Audio Jack1	φ3.5mm 3-L Jack	L/R audio out
J4	Audio Jack2	φ3.5mm 3-L Jack	Micphone In
J5	HDMI Rx	Type-A HDMI 1.4	HDMI 1.4 Receiver
J6	HDMI Tx1	Type-A HDMI 1.4	HDMI 1.4 Transmitter up to 4K@30HZ
J7	HDMI Tx2	Type-A HDMI 2.0	HDMI 2.0 Transmitter up to 4K@60HZ
J8	VGA DB15	VGA Socket	VGA Output
J9	RJ45	Gigabit Ethernet	10/100/1000-Mbps data transfer rates
J10	RJ45	Gigabit Ethernet	10/100/1000-Mbps data transfer rates
J11	GPIO	PH2.0mm 2x5pin header	PDM, SPI and GPIOs
J12	GPIO	PH2.0mm 2x20pin header	Power,I2C,UART,Can,ADC,GPIOs
J13	Update	push-button	Key for system recovery or other function
J14	Debug&Key	PH2.54mm 2x9pin Receptacle	Debug and Key and 3.3V out
J15	USB3.0	Type-A USB3.0 OTG	USB3.0 OTG
J16	USB3.0	Type-A USB3.0 host	USB3.0 host
J17	Sim card	Micro Sim card slot	Micro push-to-push sim card
J18	USB2.0_1	Type-A USB2.0 host	The first USB2.0 host for external devices
J19	USB2.0_2	Type-A USB2.0 host	The second USB2.0 host for external devices
J20	Work LED	Green led *2	Work status and 3G/4G Module Status Indicator
J21	Power LED	Red and Green LEDs	Power status indicate
J22	LVDS	PH2.0mm 2x20pin header	Dual channel 24bit LVDS output
J23	ADC/Audio	PH2.0mm 2x5pin header	ADC In and Audio Out
J24	JUMPER	PH2.0mm 2x2pin header	Voltage Select for LVDS Panel Power
J25	LCM_BL	PH2.0mm 6pin wafer	The first LCM backlight control
J26	M.2 M-key	Standard M.2 M-key connector	M.2 NGFF ( M-KEY ) with PCIE V3.0 x2
J27	Mini-PCle-1	Mini-PCle 52pin socket	For 2G/3G/4G LTE module used
J28	MIPI_RX	30pin 0.5mm pitch FPC connector	MIPI-CSI 4lane or 2*2Lane for external cameras
J29	RTC battery	CR1220 Socket	RTC battery power input 3.0V
J30	ANT2	I-PXE,MHF Φ=2.0	RF Port 2 for Bluetooth
J31	ANT0	I-PXE,MHF Φ=2.0	RF Port 0 for WiFi
J32	ANT1	I-PXE,MHF Φ=2.0	RF Port 1 for Wifi
J33	Mini-PCle-2	Mini-PCle 52pin socket	For USB3,0 AI Card 1
J34	Mini-PCle-3	Mini-PCle 52pin socket	For USB3,0 AI Card 2
J35	TF Card	Push-Push TF socket	TF Card

## 6. Pin Definition

### DC Jack1 (J1)

Pin number	Pin name	Voltage level	Notice
1	DC-IN	12V	-
2	DC-IN	12V	-
3	GND	GND	-
4	GND	GND	-

### GPIO (J11)

Pin number	Pin name	Voltage level	Notice
1	PDM_CLK1_M0_SOC	3.3V	GPIO-A4-d
2	PDM_SDI3_M0_ADC	3.3V	GPIO1_B0_d
3	PDM_SDI2_M0_ADC	3.3V	GPIO1_B1_d
4	PDM_SDI1_M0_ADC	3.3V	GPIO1_B2_d
5	FSPI_D3/FLASH_CS1n	3.3V	GPIO1_D4_u
6	FSPI_CS0n/FLASH_CS0n	3.3V	GPIO1_D3_u
7	FSPI_CLK/FLASH_ALE	3.3V	GPIO1_D0_d
8	FSPI_D1/FLASH_RDn	3.3V	GPIO1_D2_u
9	GND	GND	-
10	FSPI_D0/FLASH_RDY	3.3V	GPIO1_D1_u

### GPIO (J12)

Pin number	Pin name	Voltage level	Notice
1	VCC12V_DCIN	12V	-
2	VCC12V_DCIN	12V	-
3	VCC12V_DCIN	12V	-
4	VCC12V_DCIN	12V	-
5	GND	GND	-
6	GND	GND	-
7	VCC5V0_EXT	5	5V/2A Output
8	VCC5V0_EXT	5	5V/2A Output
9	GND	GND	-
10	GND	GND	-
11	VCC3V3_EXT	+3.3V	3.3V/2A Output
12	VCC3V3_EXT	+3.3V	3.3V/2A Output
13	VCCA1V8_EXT	+1.8V	1.8V/200mA Output
14	VCCA1V8_EXT	+1.8V	1.8V/200mA Output
15	SARADC_VIN2_HP_HOOK	0~1.8V	ADC Input
16	SARADC_VIN3	0~1.8V	ADC Input
17	GND	GND	-
18	GND	GND	-

19	CAN0_TX	3.3V	Can be alternated as I2C1-SCL
20	CAN0_RX	3.3V	Can be alternated as I2C1-SDA
21	CAN1_TX	3.3V	GPIO4_C3_d
22	CAN1_RX	3.3V	GPIO4_C2_d
23	UART4_TX_M1	3.3V	GPIO3_B2_d
24	UART4_RX_M1	3.3V	GPIO3_B1_d
25	UART7_TX_M1	3.3V	GPIO3_C4_d
26	UART7_RX_M1	3.3V	GPIO3_C5_d
27	UART9_TX_M1	3.3V	GPIO4_C5_d
28	UART9_RX_M1	3.3V	GPIO4_C6_d
29	I2C5_SCL_3V3	3.3V	GPIO3_B3_d
30	I2C5_SDA_3V3	3.3V	GPIO3_B4_d
31	TP_RESET_L	3.3V	GPIO0_B6_u
32	TP_INT_L	3.3V	GPIO0_B5_u
33	GPIO3_B5	3.3V	GPIO3_B5_d
34	SPK_CTL_H	3.3V	GPIO3_C3_d
35	PCIE30X1_CLKREQn_M1	3.3V	GPIO2_D2_d
36	I2S3_MCLK_M0	3.3V	GPIO3_A2_d
37	LCD0_BL_PWM4	3.3V	GPIO0_C3_d
38	LCD1_BL_PWM5	3.3V	GPIO0_C4_d
39	GND	GND	-
40	GND	GND	-

**Update Key (J13)**

Pin number	Pin name	Voltage level	Notice
1	SARADC_VIN0	0~1.8V	Key for system recovery or other function

**Debug&Key (J14)**

Pin number	Pin name	Voltage level	Notice
1	CPU_DBG_TX	3.3V	Used for CPU debug,1.5Mbps Data rate
2	CPU_DBG_RX	3.3V	Used for CPU debug,1.5Mbps Data rate
3	VCC3V3_PMU	3.3V	3.3V/1A Output
4	GND	GND	-
5	NPU1_TX	3.3V	Reserved for NPU1 debug,1.5Mbps Data rate
6	NPU1_RX	3.3V	Reserved for NPU2 debug,1.5Mbps Data rate
7	NPU2_TX	3.3V	Reserved for NPU2 debug,1.5Mbps Data rate
8	NPU2_RX	3.3V	Reserved for NPU2 debug,1.5Mbps Data rate
9	GND	GND	-
10	GND	GND	-

11	PWR_KEY	3.3V	Power on/off signal, Pulled up internally by 30K Ohm resistor
12	GND	GND	-
13	GND	-	-
14	USB30_OTG_EN	-	Pulled up internally, Pull Low to make USB port (J15) enter OTG mode. Otherwise, It' ll keep as Device mode.
15	RST_KEY	-	Pulled up internally, Pull Low to reset the entire system.
16	GND	GND	-
17	GND	GND	-
18	UPDATE_KEY	-	Key for system recovery or other function

**Work LED (J20)**

Pin number	Pin name	Voltage level	Notice
1	LED1+	-	Green LED for 3G/4G Modue
2	LED1-	-	-
3	LED2+	-	Green LED for system status
4	LED2-	-	-

**PowerLED (J21)**

Pin number	Pin name	Voltage level	Notice
1	LED1+	-	Red LED for Power OK
2	LED1-	-	-
3	LED2+	-	Green LED for system status
4	LED2-	-	-

**LVDS (J22)**

Pin number	Pin name	Voltage level	Notice
1	VCC_LVDS	3.3V/5V optional by jumper	-
2	VCC_LVDS		-
3	VCC_LVDS		-
4	GND	GND	-
5	GND	GND	-
6	GND	GND	-
7	RXO0M	-	-
8	RXO0P	-	-
9	RXO1M	-	-
10	RXO1P	-	-
11	RXO2M	-	-
12	RXO2P	-	-
13	GND	GND	-
14	GND	GND	-



15	RXOCM	-	-
16	RXOCP	-	-
17	RXO3M	-	-
18	RXO3P	-	-
19	RXE0M	-	-
20	RXE0P	-	-
21	RXE1M	-	-
22	RXE1P	-	-
23	RXE2M	-	-
24	RXE2P	-	-
25	GND	GND	-
26	GND	GND	-
27	RXECM	-	-
28	RXECP	-	-
29	RXE3M	-	-
30	RXE3P	-	-
31	GND	-	-
32	GND	-	-
33	LVDS_BL_EN	3.3V	GPIO0_C5_d
34	LVDS_BL_PWM0	3.3V	GPIO0_C5_d
35	LVDS_IRQ	1.8V	-
36	I2C_SDA_LVDS	1.8V	-
37	LVDS_PWR_EN	-	GPIO0_C7_d
38	I2C_SCL_LVDS	1.8V	-
39	GND	-	-
40	IR_IN	-	GPIO0_C2_d

**ADC/Audio (J23)**

Pin number	Pin name	Voltage level	Notice
1	SARADC_VIN5	0~1.8V	ADC Input Channel 5
2	SARADC_VIN4	0~1.8V	ADC Input Channel 4
3	SARADC_VIN7	0~1.8V	ADC Input Channel 7
4	SARADC_VIN6	0~1.8V	ADC Input Channel 6
5	HP_SNS	Analog GND	-
6	GND	GND	-
7	HPR_OUT	Analog	HeadPhone Right Channel Output
8	HPL_OUT	Analog	HeadPhone Left Channel Output
9	SPKN_OUT	Analog	Speaker Output N (1.3W @8 Ohm Load)
10	SPKP_OUT	Analog	Speaker Output P (1.3W @8 Ohm Load)

**Jumper for LVDS Voltage Selector (J24)**

Pin number	Pin name	Voltage level	Notice
1	1,2 shorted	+5V	LVDS Panel Power(J22): +5V
2	3,4 shorted	+3.3V	LVDS Panel Power(J22): +3.3V

**LCM\_BackLight (J25)**

Pin number	Pin name	Voltage level	Notice
1	GND	GND	-
2	GND	GND	-
3	BL_ADJ	3.3V	GPIO4_C2_D/PWM0
4	BL_EN	3.3V	GPIO4_C6_D/PWM1
5	VCC12V_BL	12V	-
6	VCC12V_BL	12V	-

**M.2 M-key (J26)**

Pin number	Pin name	Voltage level	Notice
1,3,9,15,21,27,33,39,45,51,57,6 3,65,67	GND	GND	-
2,4,12,14,16,18,62,64,66	VCC3V3	+3.3V	-
29	PCIE30_RX1N	-	-
31	PCIE30_RX1P	-	-
35	PCIE30_TX1N	-	-
37	PCIE30_TX1P	-	-
41	PCIE30_RX0N	-	-
43	PCIE30_RX0P	-	-
47	PCIE30_TX0N	-	-
49	PCIE30_TX0P	-	-
53	PCIE30_REFCLKN	-	-
55	PCIE30_REFCLKN	-	-
38	DEVSLP	3.3V	Pulled up by 10K Ohm Resistor to 3.3V
50	PCIE30X2_PERSTn_3V3_L	3.3V	-
52	PCIE30X2_CLKREQn_3V3_L	3.3V	-
54	PCIE30X2_WAKEn_3V3_L	3.3V	-
All the other pins	NC	-	Not Connected

**Mini-PCIe-1 (J27)**

Pin number	Pin name	Voltage level	Notice
2,24,39,41,52	VCC3V6_4G	+3.6V	Power Supply for 4G module
4,9,15,18,21,26,27,29,34,35,37, 40,43,50	GND	GND	-
8	SIM_VCC	1.8/3.3V	Depending on the Module
10	4G_SIM_SIO	SIM_VCC	-
12	4G_SIM_CLK	SIM_VCC	-
14	4G_SIM_RST	SIM_VCC	-
22	4G_RESET	OC	GPIO2_D0_d Active High
36	4G_USB_DM	-	-
38	4G_USB_DP	-	-
42	4G_LED	Current Sink	-

17	4G_USB_SSRXN	-	-
19	4G_USB_SSRXP	-	-
31	HOST_WAKEUP_4G	OC	GPIO2_D1_d Active High
49	4G_USB_SSTXN	-	-
51	4G_USB_SSTXP	-	-
All the other pins	NC	-	Not Connected

**MIPI\_RX (J28)**

Pin number	Pin name	Voltage level	Notice
1,4,7,10,13,16,19	GND	GND	-
2	MIPI_CSI_RX_D0P	-	-
3	MIPI_CSI_RX_D0N	-	-
5	MIPI_CSI_RX_D1P	-	-
6	MIPI_CSI_RX_D1N	-	-
8	MIPI_CSI_RX_CLK0N	-	-
9	MIPI_CSI_RX_CLK0P	-	-
11	MIPI_CSI_RX_D2P	-	-
12	MIPI_CSI_RX_D2N	-	-
14	MIPI_CSI_RX_D3P	-	-
15	MIPI_CSI_RX_D3N	-	-
17	MIPI_CSI_RX_CLK1P	-	-
18	MIPI_CSI_RX_CLK1N	-	-
20	CIF_CLKOUT	-	-
21	MIPI_CAM_X2_RST0	1.8V	GPIO3_D4_d
22	MIPI_CAM0_PDN_L	1.8V	GPIO3_D5_d
23	MIPI_CAM_X2_RST1	1.8V	GPIO3_D2_d
24	MIPI_CAM1_PDN_L	1.8V	GPIO3_D3_d
25	I2C_SCL_CAM	1.8V	GPIO4_B5_d
26	I2C_SDA_CAM	1.8V	GPIO4_B4_d
27	VCC1V8_DOVDD_DVP0	1.8V	Power Supply for Camera I/O
28	VDD1V2_DVDD_DVP0	1.2V	Power Supply for Camera Digital circuits
29	VCC2V8_DVP0	2.8V	Power Supply for Camera (500mA MAX)
30	VCC2V8_AVDD_DVP0	2.8V	Power Supply for Camera Analog circuits

**Mini-PCIe-2 (J33)**

Pin number	Pin name	Voltage level	Notice
2,24,39,41,52	VCC3V3_NPU	+3.3V	Power Supply for AI module
4,9,15,18,21,26,27,29,34,35,37, 40,43,50	GND	GND	-
17	HUB_USB3_SSRXN	-	-
19	HUB_USB3_SSRXP	-	-
22	NPU_RESETh	OC	GPIO3_A1_d Active High

30	-	-	-
32	-	-	-
36	HUB_USB3_DM	-	-
38	HUB_USB3_DP	-	-
45	NPU_RESETn	OC	GPIO3_A1_d Active High
49	HUB_USB3_SSTXN	-	-
51	HUB_USB3_SSTXP	-	-
All the other pins	NC	-	Not Connected

**Mini-PCIe-3 (J34)**

Pin number	Pin name	Voltage level	Notice
2,24,39,41,52	VCC3V3_NPU	+3.3V	Power Supply for AI module
4,9,15,18,21,26,27,29,34,35,37, 40,43,50	GND	GND	-
17	HUB_USB2_SSRXN	-	-
19	HUB_USB2_SSRXP	-	-
22	NPU_RESETn	OC	GPIO3_A1_d Active High
30	-	-	-
32	-	-	-
36	HUB_USB2_DM	-	-
38	HUB_USB2_DP	-	-
45	NPU_RESETn	OC	GPIO3_A1_d Active High
49	HUB_USB2_SSTXN	-	-
51	HUB_USB2_SSTXP	-	-
All the other pins	NC	-	Not Connected

# 7.Application Scenarios



**AI**



**Machine Vision**



**Industrial Control**



**Energy and Power**



**Smart Tablet**



**VR**



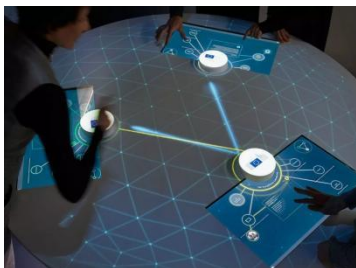
**Smart Logistics**



**New**



**Smart Commercial**



**Object Recognition**



**Vehicle terminal**



**Security Surveillance**

## 8. Ordering Model

Product Model	Status	CPU	DDR	eMMC	Operating Temperature
LZ12021600	ACTIVE	RK3568	2GB	16GB	-10°C - 70°C
LZ12043200	ACTIVE	RK3568	4GB	32GB	-10°C - 70°C
LZ12083200	ACTIVE	RK3568	8GB	32GB	-10°C - 70°C
LZ12121600	ACTIVE	RK3568	2GB	16GB	-20°C - 70°C
LZ12143200	ACTIVE	RK3568	4GB	32GB	-20°C - 70°C
LZ12183200	ACTIVE	RK3568	8GB	32GB	-20°C - 70°C

\*For customized non-standard orders, please contact us via email at [sales@neardi.com](mailto:sales@neardi.com).

# 9.About NearDi














Shanghai NearDi Technology Co., Ltd., established in 2014, is a national-level high-tech enterprise, a strategic partner of Rockchip, and an authorized agent for Black Sesame Technologies. We focus on the research and development and production of enterprise-level open-source hardware platforms, offering customers core modules, industry-specific boards, development boards, touch panels, and industrial control hosts. Adhering to the core philosophy of technological innovation and professional service, leveraging NearDi Technology's technical strengths and industry experience, we assist our partners in achieving rapid mass production of their products.

## Company Advantages

Software Design / Custom OS / Product ODM / Bulk Delivery

## Products

### Rockchip

System On Module				
 LCB3588/J	 LCB3568/J	 LCB3566	 LCB3399Pro	 LCB3399
Development Board				
 LKD3588/J	 LKD3568/J	 LKD3566	 LKD3399Pro	 LKD3399
Embedded Computer				
 LPB3588	 LPM3588	 LPC3588	 LPB3568	 LPB3399Pro






### Black Sesame Technologies

 SOM-A-A1000	 SOM-π-A1000	 SOM-B-A1000	 SOM-A1000 开发者套件
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### Vehicle Terminal

 LPA3588	 LPA3568	 LPA3399Pro	 LPS3399Pro
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### WIFI Module

 FD7352S	 FD7352P	 FD7352M	 FD7155U	 FD7256S
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